Achieving the Theoretical Depairing Current Limit in Superconducting Nanomesh Films

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ABSTRACT We show the theoretical depairing current limit can be achieved in a robust fashion in highly ordered superconductor nanomesh films having spatial periodicities smaller than both the superconducting coherence length and the magnetic penetration depth. For a niobium nanomesh film with 34 nm spatial periodicity, the experimental critical current density is enhanced by more than 17 times over the continuous film and is in good agreement with the depairing limit over the entire measured temperature range. The nanomesh superconductors are also less susceptible to thermal fluctuations when compared to nanowire superconductors. $T_c$ values similar to the bulk film are achieved, and the nanomeshes are capable of retaining superconductivity to higher fields relative to the bulk. In addition, periodic oscillations in $T_c$ are observed as a function of field, reflecting the highly ordered nanomesh structure.

KEYWORDS Superconductors, nanomaterials, electronic materials, nanofabrication

The highest dissipationless current (supercurrent) a superconductor can carry is described by the depairing mechanism: when the kinetic energy associated with the supercurrent exceeds the condensation energy (e.g., the binding energy of Cooper pairs), superconductivity vanishes. However, the experimental $J_c$ (critical current $I_c$, divided by the cross section area) of bulk superconductors and superconductor films is typically more than 1 order of magnitude lower than this theoretical limit, the depairing current density ($J_{dp}$). Improved $J_c$ has been obtained through patterning superconductors at micrometer and submicrometer scales, but the depairing limit is still not reached, and significantly increased current densities are only evident close to the critical temperature ($T_c$).

The reasons behind the large discrepancy between the experimental $J_c$ and the theoretical $J_{dp}$ are 2-fold. First, transverse dimensions larger than the magnetic penetration depth $\lambda$ lead to the piling-up of currents at the surfaces and/or edges of the superconductor due to the Meissner effect. Second, for transverse dimensions larger than the superconducting coherence length $\xi$, vortices nucleate within the superconductor at high currents, the motion of which leads to dissipation and thus destruction of superconductivity. Superconductor wires having diameter smaller than both $\xi$ and $\lambda$ can, in principle, overcome both these limitations. Because both $\xi$ and $\lambda$ are large for temperature ($T$) close to $T_c$, early studies indicated that $J_{dp}$ can be achieved in micrometer-size filaments for $T \sim T_c$. However, in this regime $J_c$ is much lower than its low-$T$ limit. For $T < 0.9T_c$, both $\xi$ and $\lambda$ quickly reach their low-$T$ limits of ~50 nm, and $J_c$ falls far below $J_{dp}$.

Nanofabrication advances have permitted the exploration of superconductor nanowires (NWs) with transverse dimensions smaller than the low-$T$ limits of $\xi$ and $\lambda$. However, in this regime, NWs are quasi-one-dimensional, meaning that only one pathway is available for supercurrent passage. Consequently, supercurrents in NWs are readily disrupted by transient, local resistive “phase-slip” events due to thermal and quantum fluctuations.

Here we report on a novel film structure that harnesses the advantages of NWs while circumventing the fluctuation effects characteristic of quasi-1D superconductors. This is achieved by building a highly ordered two-dimensional nanomesh film (Figure 1) with characteristic length scales well below both $\xi$ and $\lambda$. In such structures, the current-carrying components are effectively NWs, so supercurrents over the cross section of each NW are uniform, and vortices cannot form within the NWs. Meanwhile, the small dimensions of the nanomesh lattice provide alternative pathways for supercurrents within distances that are well below the superconducting coherence length $\xi$. This means that the supercurrents can avoid transient local resistive events caused by thermal fluctuations. As a result, a large enhancement of $J_c$ is observed, and the $J_{dp}$ limit is achieved over a wide temperature range.

Nb nanomesh films were prepared using superlattice nanowire pattern transfer (SNAP), which translates the atomic control over the layer thicknesses within a thin film superlattice into control over the width and spacing of NWs. Briefly, two sets of Pt SNAP NWs were sequentially deposited onto a SiO$_2$-coated superconductor (Nb) thin film (thickness = 11 nm) to form a crossbar structured etch mask (Figure

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Highly directional dry etching converted the Nb film into the nanomesh films shown in Figure 1b–d. The nanomesh devices were patterned to be ∼5 µm × ∼5 µm in area, with voltage probes made out of the same Nb nanomesh for four-point measurements (Figure 1b and Supplementary Figure 1 in the Supporting Information). See Supporting Information for detailed methods. To interrogate dimensional effects, we prepared different pitch nanomesh films (34 and 75 nm) to bracket the Nb (∼40 nm). The widths of the nanomesh grid lines were similar (16 and 15 nm) for both films. The sizes of the square voids in the nanomesh films are thus 18 and 60 nm, respectively. The electrical properties of the nanomesh films were characterized and compared with a 10 µm wide continuous thin film device, which was patterned from the same 11 nm thick starting Nb film as the nanomesh films.

Figure 2a provides the measured temperature-dependent resistance ($R$) of different devices at varied magnetic fields. Remarkably, at zero field, the $R$–$T$ behavior of the 34 nm pitch nanomesh (henceforth referred to as “34 nm nanomesh”) is similar to that of the film. This contrasts with the $R$–$T$ behavior of individual NWs, which is characterized by significantly reduced $T_c$, a severely broadened transition temperature width, and large residual resistance for $T < T_c$. This result demonstrates that nanomesh films patterned at pitches smaller than $\xi$ are not susceptible to thermal fluctuations, as supercurrents can readily find alternative pathways through interconnecting NWs when fluctuation-induced local resistance occurs. The larger pitch 75 nm nanomesh exhibits a modest reduction of $T_c$ and a slightly widened transition temperature width due to fluctuations. Meanwhile, in the presence of perpendicular magnetic fields ($H$), the nanomesh films retain superconductivity to higher fields: at $H = 1$ T, both nanomesh films have $T_c$ higher than the film. At $H = 2$ T, complete superconductivity is not achieved in the bulk film at base temperature (1.7 K), while it is achieved in nanomesh films at $T > 3$ K. This is likely because the grid wires within the nanomesh have cross sections smaller than $\xi$ and $\lambda$, consistent with previous studies on individual NWs.

The influence of $H$ is also clearly seen in the $T_c$–$H$ data (Figure 2b) summarized from multiple $R$–$T$ curves collected at closely spaced $H$ (Supplementary Figure 2 in the Supporting Information). Due to the finite transition temperature width, a resistance criterion is used here to define $T_c$, i.e., $T_c$ is defined as the temperature at which the measured resistance is 50% of the normal state resistance, $R_N$. Enhanced $T_c$ relative to the film is observed for the 34 nm nanomesh for all $H$, while the 75 nm nanomesh has reduced $T_c$ at low $H$ due to the larger pitch, consistent with previous discussions. In addition, on top of the general decreasing trend of $T_c$ for increasing $H$, noticeable oscillating features are observed at certain fields for both nanomesh films.

Figure 2c presents the $T_c$ oscillations of the 75 nm nanomesh obtained from $T_c$–$H$ curves after subtracting a smooth, parabolic $T_c$–$H$ trend. When 0.95$R_N$ is used as the criterion for determining $T_c$, four clear, ordered oscillation periods are observed, with peaks appearing at integral
multiples of the period, \( H_1 \) = 0.362 T. When multiplied by the square of the 75 nm pitch, this value corresponds to the field at which the magnetic flux through each unit cell (loop) is 2.04 \( \times 10^{-15} \) Wb, a value that is in good agreement with the magnetic flux quantum, \( \Phi_0 = \hbar/2e = 2.068 \times 10^{-15} \) Wb. This indicates that the oscillation originates from the Little–Parks oscillations of the loops in the nanomesh: peaks in \( T_c \) occur at fields at which the flux through each loop is an integral multiple of \( \Phi_0 \), so fluxoid quantization is achieved by \( H \) alone. Similar oscillations have been reported in superconductor micropatterned networks and films patterned with hole arrays, but only for \( T \sim T_c \) and low \( H \). The oscillations of our devices extend to much higher \( H \) due to the smaller length scales we have patterned. When smaller resistance criteria are used to determine \( T_c \), the oscillations at higher \( H \) become less prominent, but small peak features emerge at \( H \) corresponding to fractional numbers of flux quanta per unit cell, in particular for \( H = H_1/2 \). These features reflect the collective behavior of unit cells in highly ordered networks, i.e., fluxoid quantization in superunit cells. \( T_c \) oscillations are also observed in the 34 nm nanomesh, but with even larger oscillation amplitude and period (Figure 2d). The observed period \( H_1 = 1.65 \) T corresponds to a matching pitch of 35.4 nm for square networks, in agreement with the designed dimensions. Fractional matching fields are clearly observed at \( H = H_1/2 \), and possibly at \( H = 1/3 \) and \( 2/5 H_1 \). Oscillations at \( H > 2H_1 \) are not measurable because \( H_1 \) is too high: at our base temperature, superconductivity is suppressed for \( H > \sim 3 \) T (cf. Figure 2a). The observed oscillations reflect the tightly controlled structural parameters of our nanomesh films.

The critical current data are summarized in Figure 5. \( J_c \) of the bulk film device is determined to be 3.2 \( \times 10^6 \) A/cm\(^2\) at base temperature. This value is in good agreement with previous studies on Nb films, but more than 1 order of magnitude lower than \( J_{dp} \) (Figure 3a). We note that a 20 nm thick Nb film is measured in their study, which has a higher \( T_c \) than the 11 nm films we used. To compensate for this difference, we have uniformly shifted the \( J_c - T \) curve to lower \( T \) by 1.15 K, so zero critical current is achieved at the \( T_c \) of our film device. See Supplementary Figure 3 (Supporting Information) for an alternative treatment in which \( T \) is rescaled by the \( T_c \) of each device.

FIGURE 2. Temperature-dependent electrical resistance of the Nb nanomesh films and corresponding thin film. (a) Temperature-dependent resistance \((R)\) of different devices measured at applied (perpendicular) magnetic fields \((H)\) of 0, 1, 2, and 3 T: red, 34 nm pitch nanomesh; blue, 75 nm pitch nanomesh; black, thin film device. The resistance is normalized to the respective normal-state resistance \((R_N)\) for each device. (b) Magnetic field-dependence of \( T_c \) for the three devices, as defined by the temperature at which \( R = 0.5R_N \). (c) The \( T_c \) oscillations of the 75 nm pitch nanomesh as a function of \( H \), obtained from the \( T_c - H \) curve after subtracting a smooth, parabolic \( T_c - H \) trend. (d) The \( T_c \) oscillations of the 34 nm pitch nanomesh as a function of \( H \). For both (c) and (d), pink, black, and green curves are obtained with the criteria \( R = 0.95R_N \), 0.5\( R_N \), and 0.05\( R_N \), respectively.
fitting parameters (Figure 3a and Supplementary Figure 3 in the Supporting Information). We emphasize that the \( I_c \) and \( J_c \) reported here were obtained through quasi-steady dc measurements, in which a continuously applied current was monotonically increased in small steps until the device became resistive. The measured \( I_c \) was stable, independent of scanning rate, and superconductivity can be maintained for prolonged periods of time for any \( I < I_c \). As shown in Figure 3b, the nanomesh switches to the normal state at the same \( I_c \) for widely different current scan rates. Before this switching occurs, the device remains in the superconducting state without detectable voltage over the \( \sim 30 \text{nV noise floor} \), even for \( I \) very close to \( I_c \). This is fundamentally different from the \( J_{dp} \) determined using pulse-current methods,\(^{30,31} \) in which short (\( \sim 1 \text{ ms} \)) current pulses are employed to eliminate the effects of dissipation due to vortex motion.

The \( I_c \) enhancement of the 75 nm nanomesh film is below that of the 34 nm nanomesh. Although the measured \( J_{cw} \) at base temperature (\( 1.9 \times 10^7 \text{ A/cm}^2 \)) is still 6-fold higher than that of the film, it is about one-third of \( J_{dp} \). This likely arises from multiple effects. First, \( T_c \) is reduced for this nanomesh, and so the onset of supercurrents occurs at lower \( T \). When the \( J_{dp} - T \) curve is rescaled to compensate for this \( T_c \) difference, very good initial agreement between \( J_{cw} \) and \( J_{dp} \) is observed for \( T > \sim 0.8T_c \), or within \( \sim 1 \text{ K} \) of \( T_c \) (Supplementary Figure 3 in the Supporting Information). At lower \( T \), however, \( J_{cw} \) increasingly deviates from (and falls below) \( J_{dp} \) with decreasing \( T \). This is likely because \( \xi \) is large at \( T \sim T_c \), so the device behaves similar to the 34 nm nanomesh. At low \( T \), \( \xi \) is small and the device starts to imitate the lower \( J_c \) of quasi-1D nanowire superconductors.

For potential applications, we note that when reduced to the entire widths of the devices, the corresponding \( J_{cd} \) is reduced relative to \( J_{cw} \) due to the presence of voids. For the 34 nm nanomesh, \( J_{cd} = \left(16/34\right)J_{cw} \sim J_{dp}/2 \). At base temperature, \( J_{cd} = 2.6 \times 10^7 \text{ A/cm}^2 \) is still more than 8-fold higher than that of the starting film. In fact, a significant enhancement of \( J_{cd} \) relative to the film is achieved over the entire temperature range (Figure 3a), in particular for low \( T \), where \( J_c \) is high. This is in contrast with previous studies on micropatterned superconductors,\(^4-8 \) in which \( J_c \) enhancement is only prominent for \( T \) close to \( T_c \), where \( \xi \) and \( \lambda \) are large and comparable with the pattern dimensions. \( J_{cd} \) of the 75 nm nanomesh is only slightly higher than the film due to the lower \( J_{cw} \) and small superconductor occupation ratio (15/75). These results indicate that to achieve the highest possible \( I_c \) within a given device width, nanomesh films patterned at ultrahigh densities are required. More generally, these results demonstrate that the theoretical depairing limit for stable and continuous supercurrent carrying capacity within a superconductor may be achieved through appropriate structural design of the superconductor.

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**Supporting Information Available.** Description of the fabrication processes and additional discussions. This material is available free of charge via the Internet at http://pubs.acs.org.

**REFERENCES AND NOTES**


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